

FIG. 1

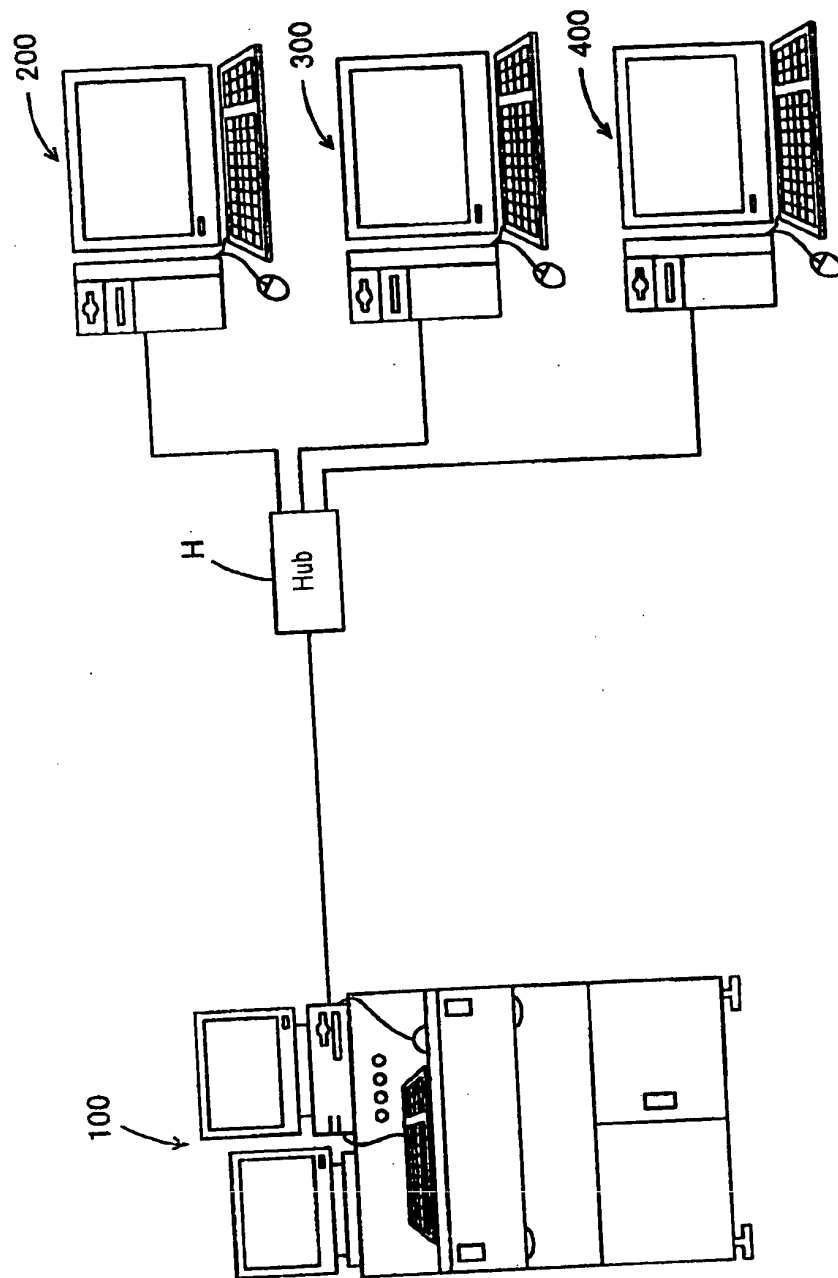


FIG. 2

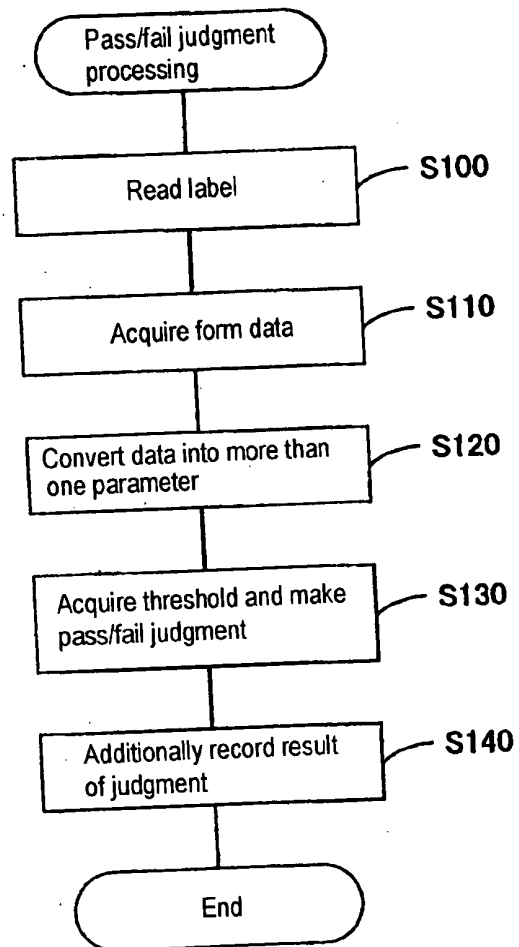


FIG. 3

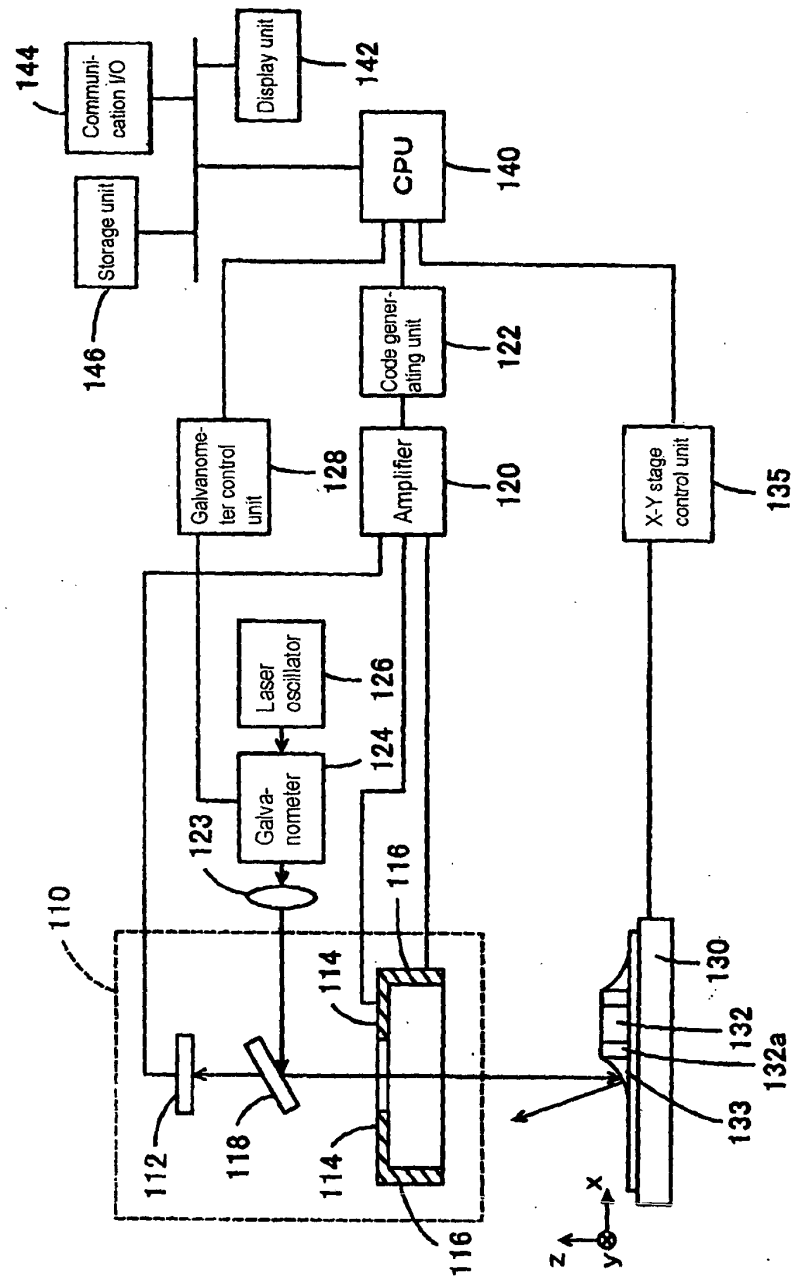


FIG. 4

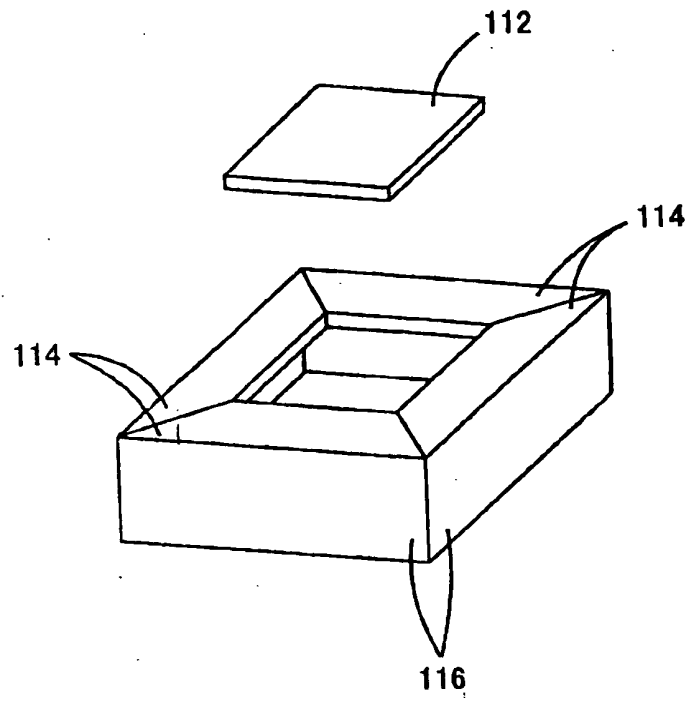


FIG. 5

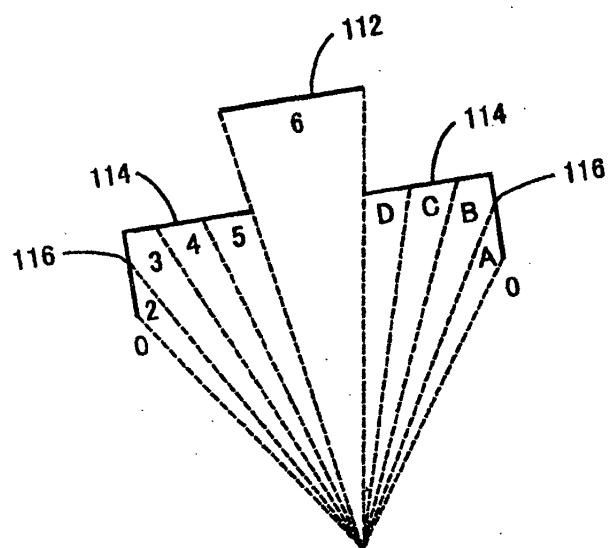


FIG. 6

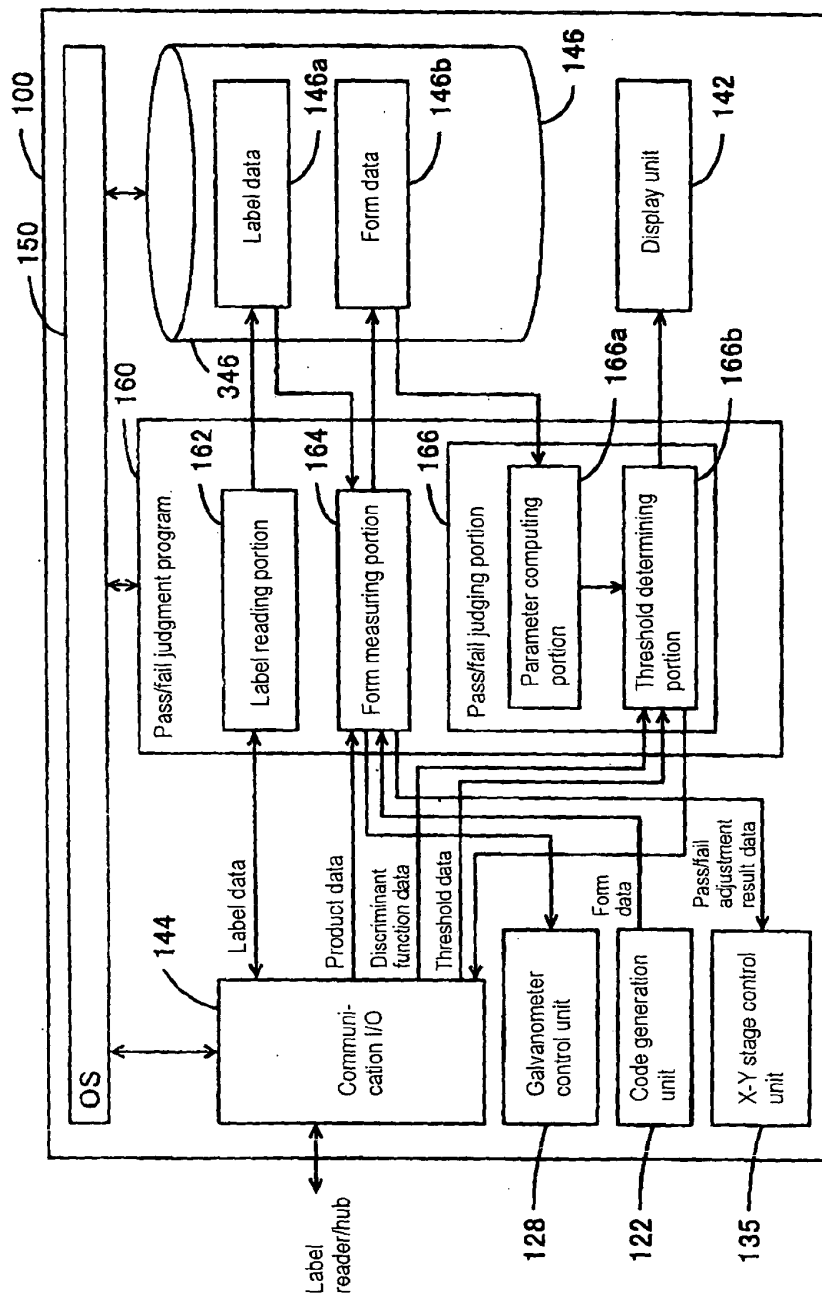


FIG. 7

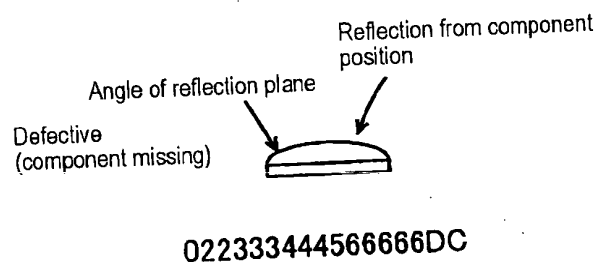
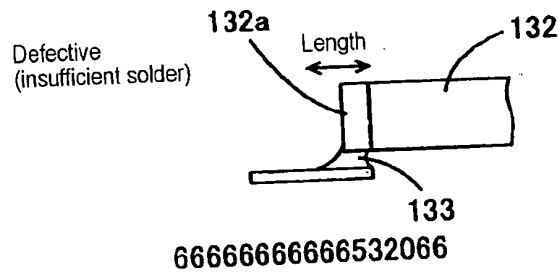
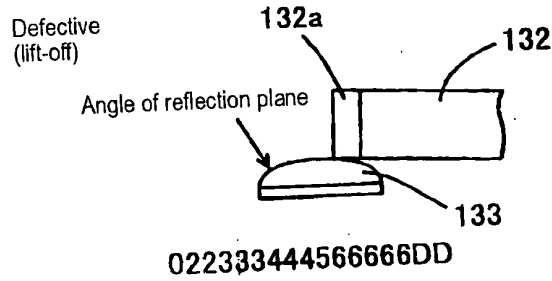
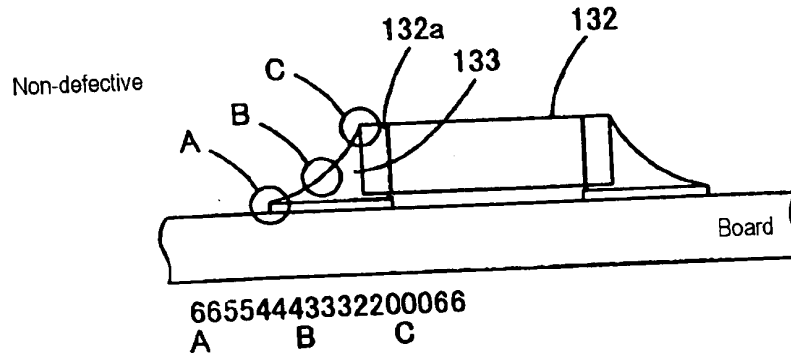


FIG. 8

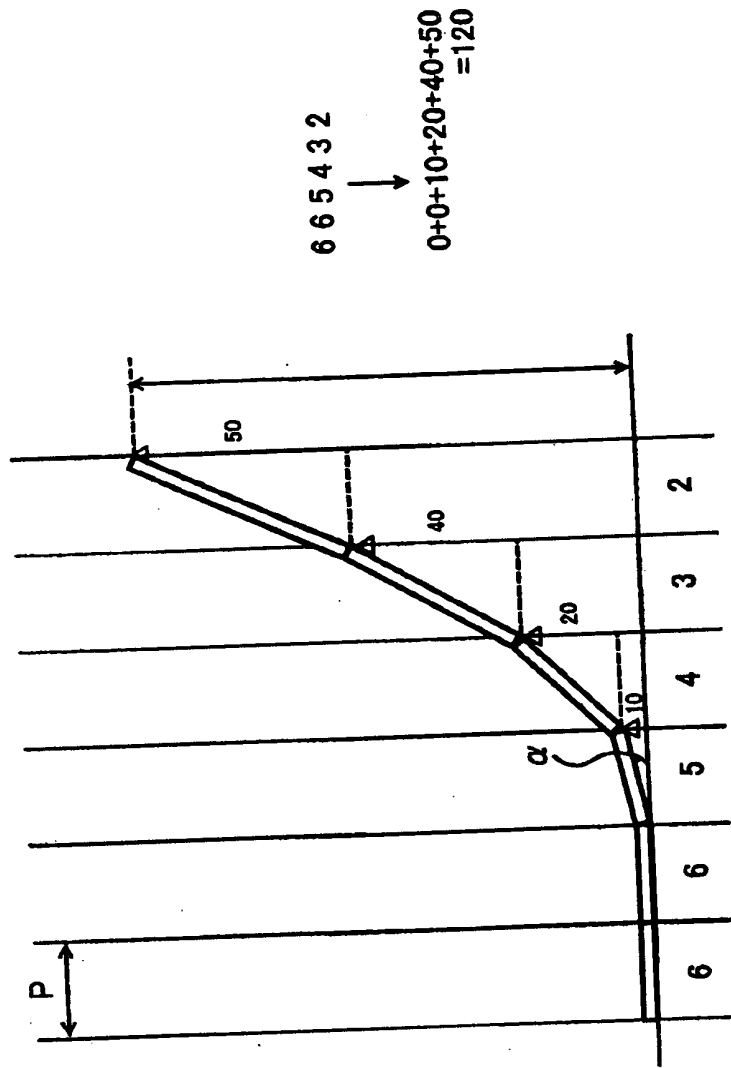


FIG. 9

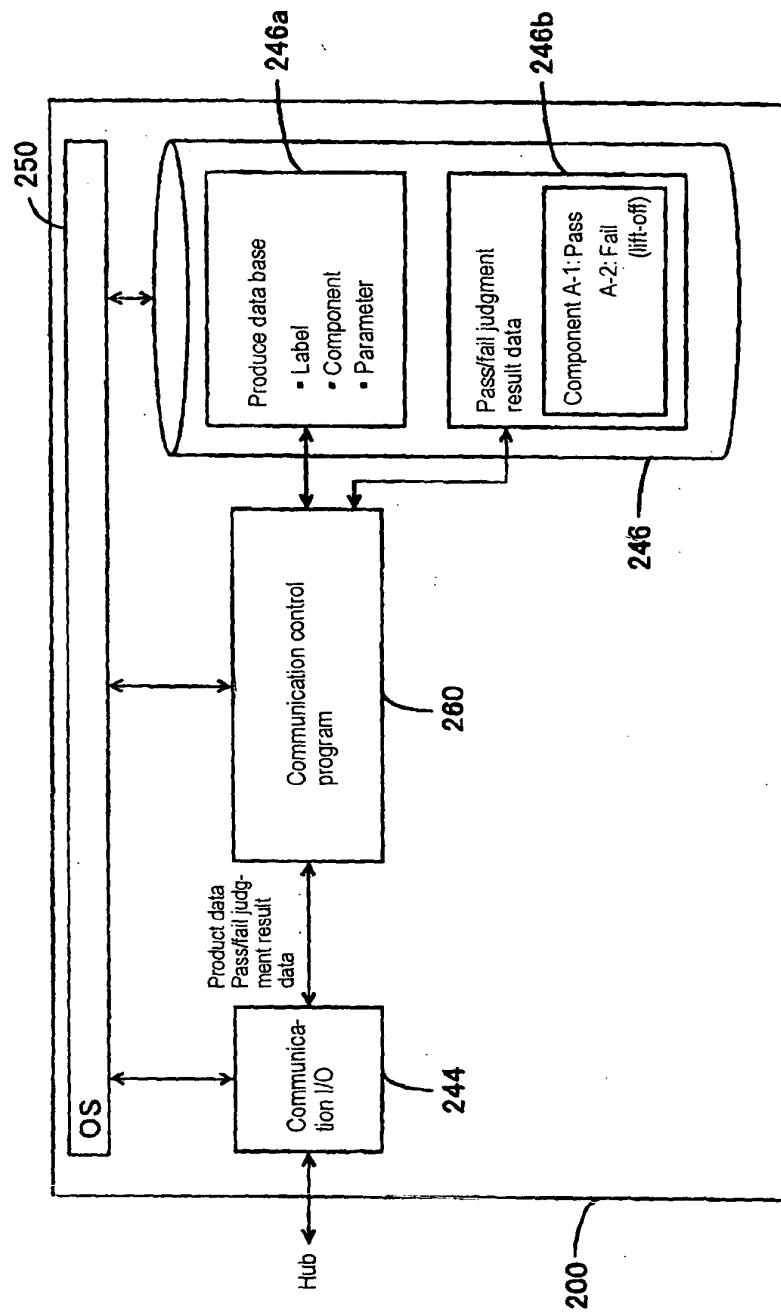


FIG. 10

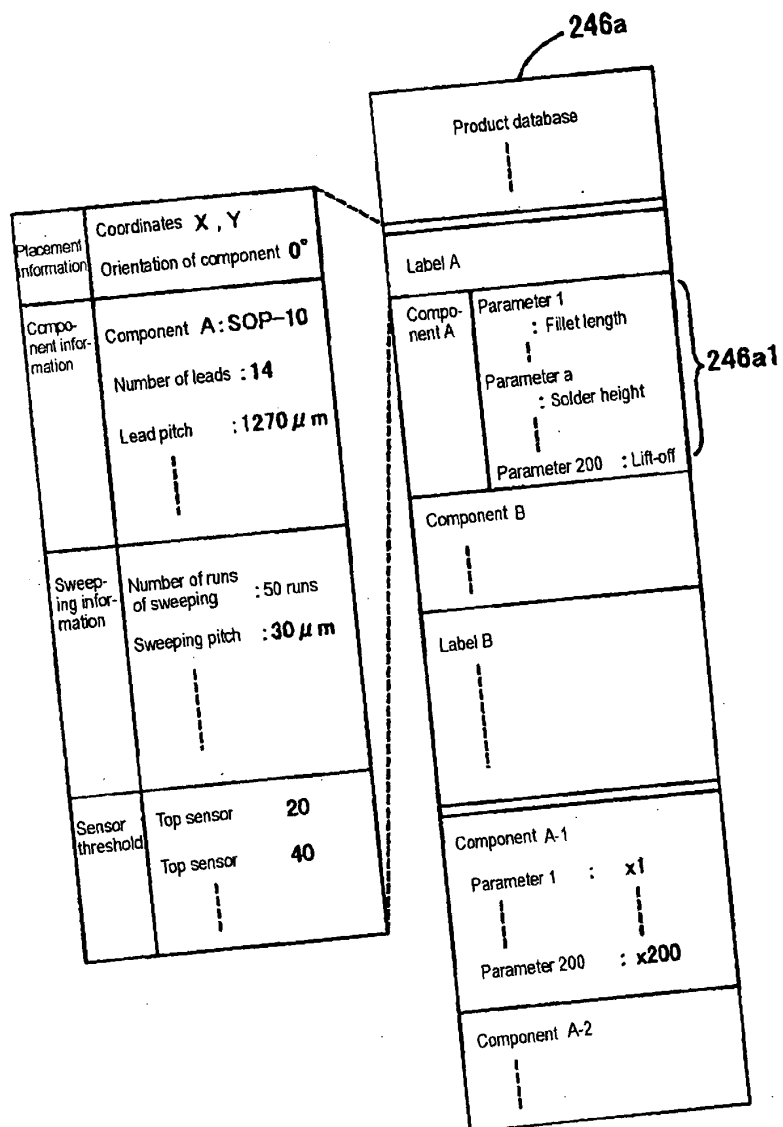


FIG. 11

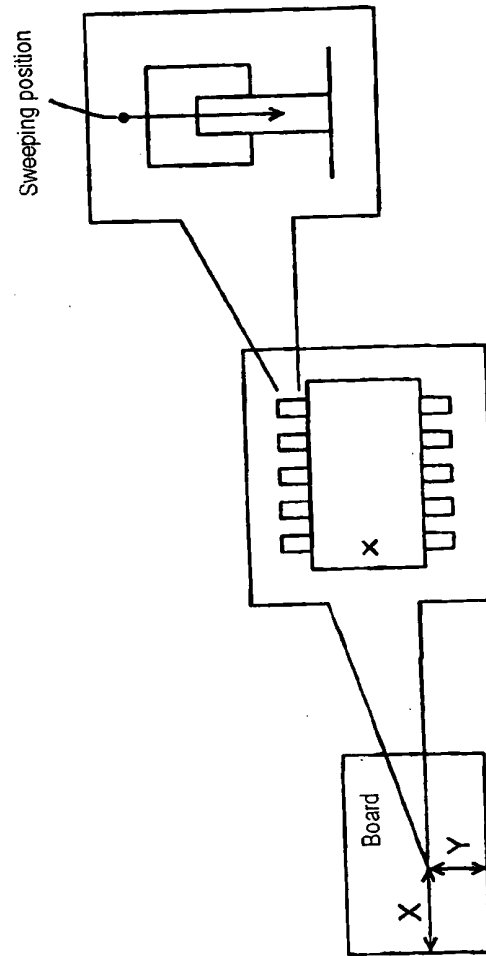


FIG. 12

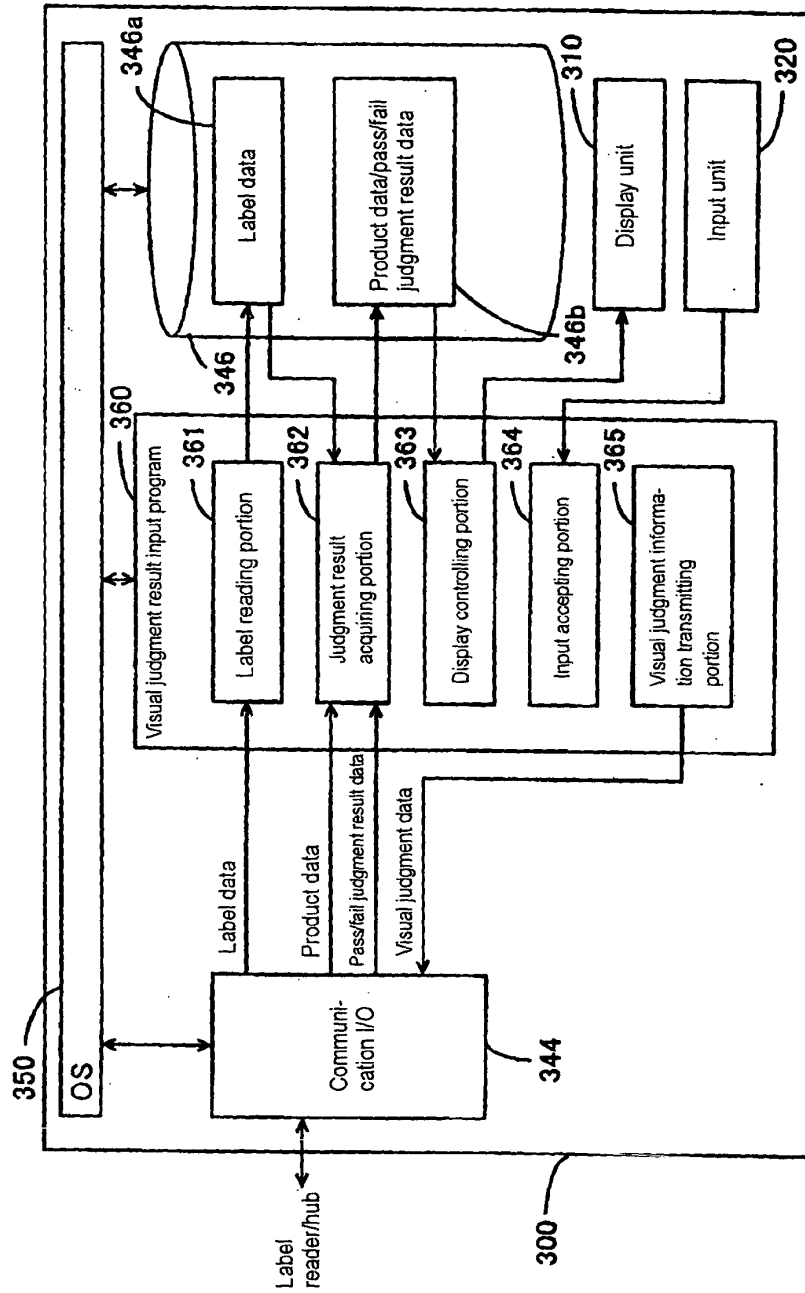


FIG. 13

Visual Judgment Result Input

Zooming component image

ZOOM

Confirmed

NG information

Disinformation

Pin No

Total

1

5

All NC		All disinformation	
NG type	NG code	Disinformation	Pin No
No	0	NG type	OP
1	1	Disinformation	1
2	2	BRQ	0
3	3	Unwetting	0
4	4	Lead displacement	0
5	5	Lead lift-off	0
6	6	Component missing	2
7	7	Primary error	0
8	8	Lead missing	0
9	9	Component displacement	0
10	10	Blow hole	0
11	11	Too small fillet	0
12	12	Excessive solder	0
13	13	No accordance	0
14	14	Analysis error	0
15	15	Spare	0
16	16	Spare	0
17	17	Spare	0
18	18	Spare	0
19	19	NO COUNT	0
20	20	Trend toward defective	0
21	21	Discard	0
PIN TOTAL		2	2

Judgment

Confirmed NG

OK

Component type

38

CHIP

Comment

P2B3

Print No

Un-Inspected

Confirmed

Disinformation

FIG. 14

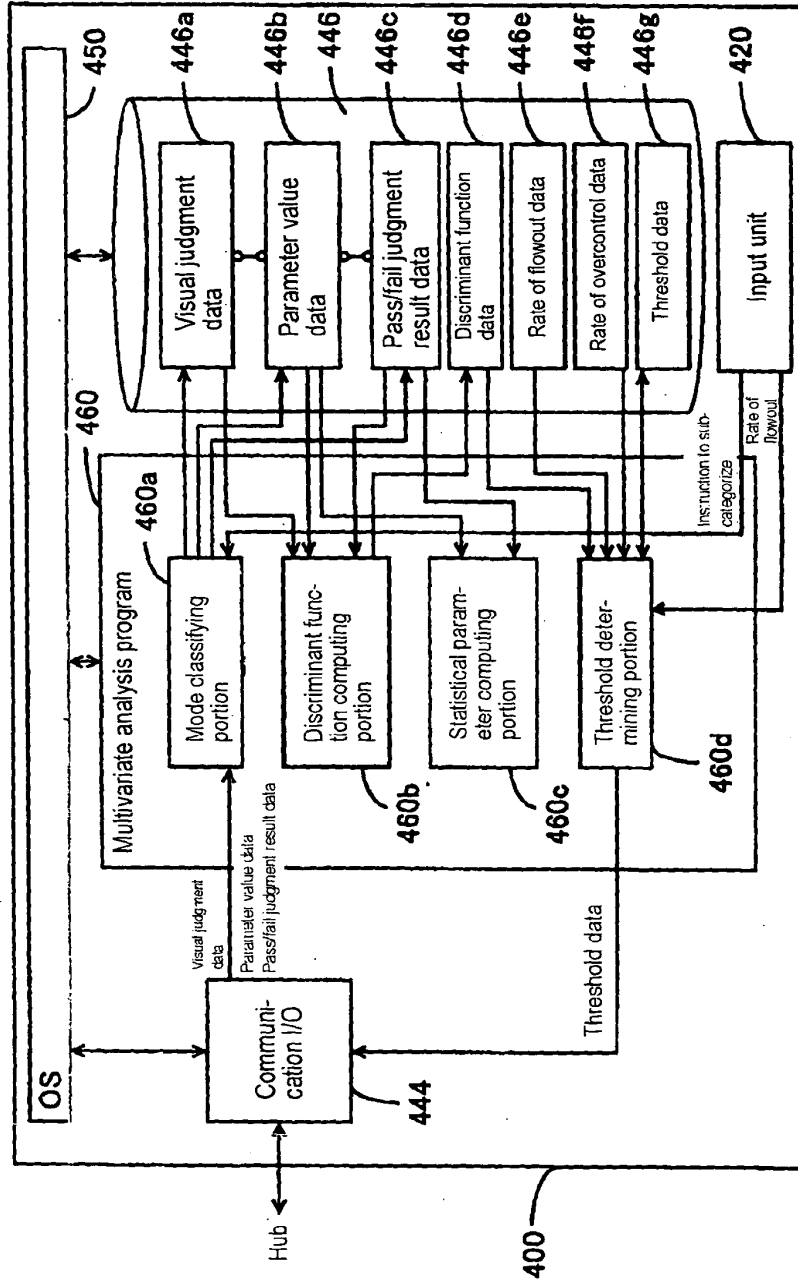


FIG. 15

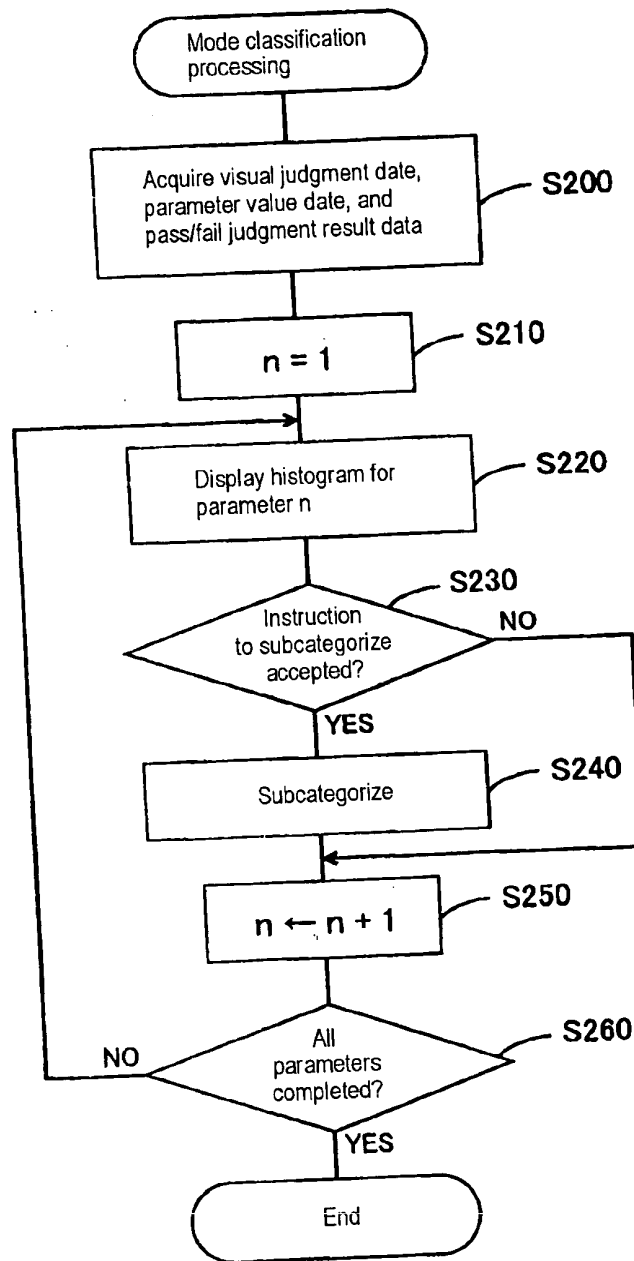


FIG. 16

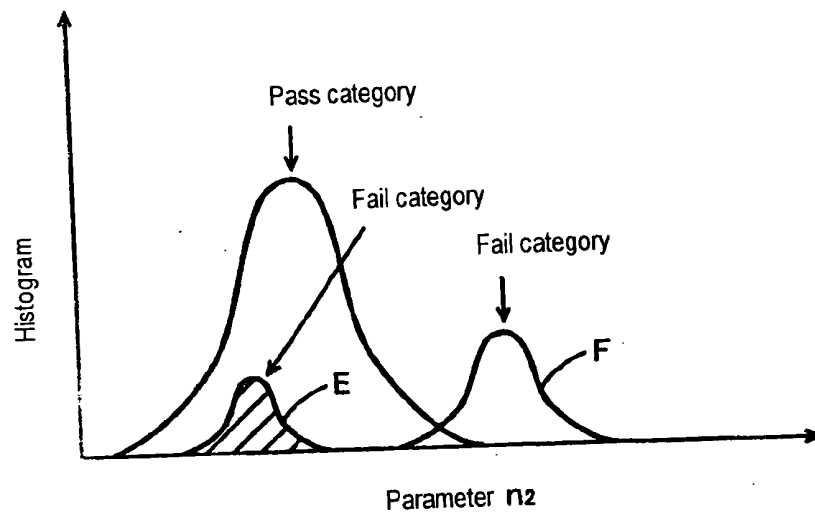
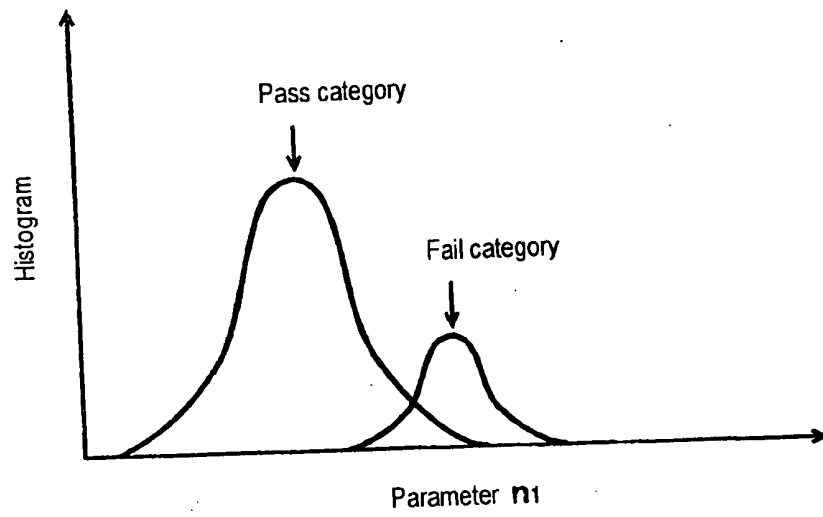


FIG. 17

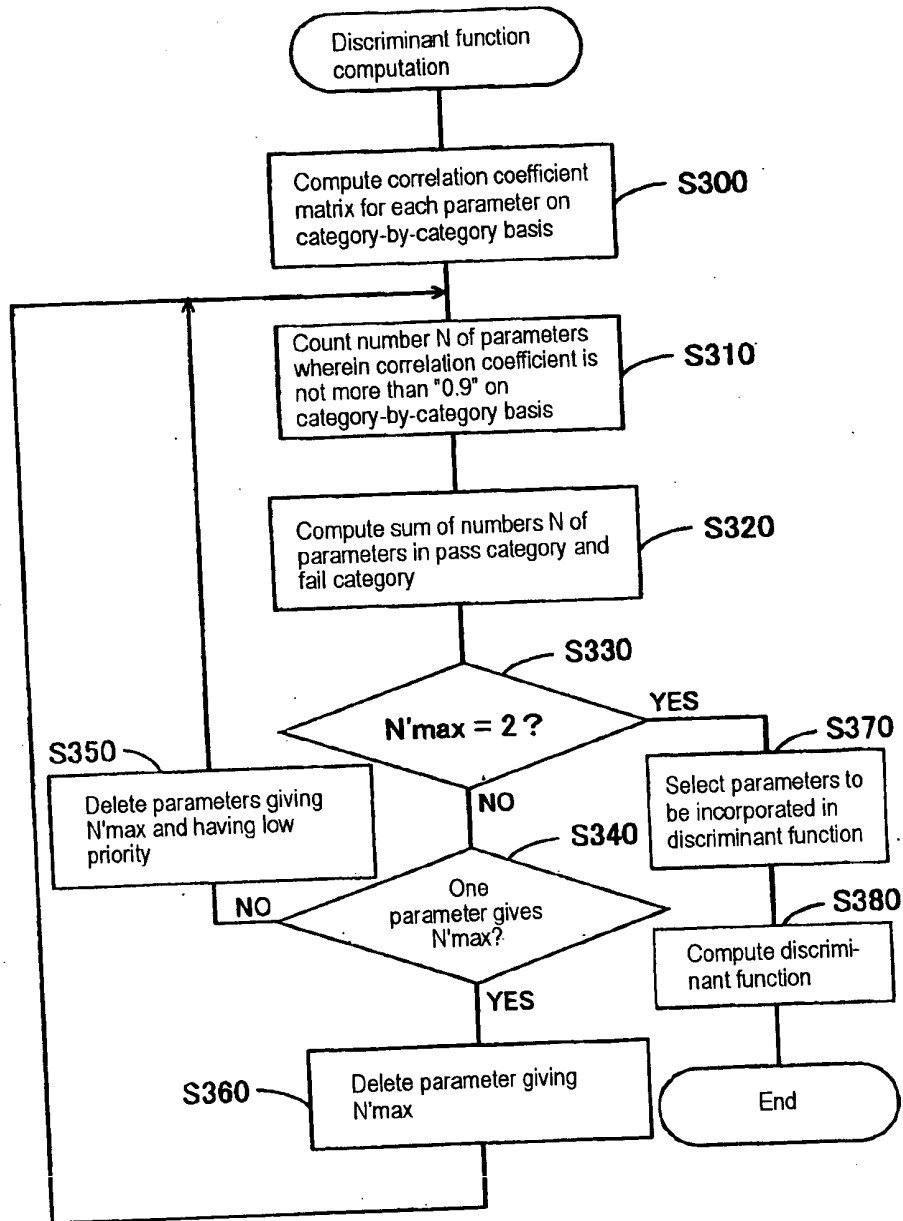


FIG. 18

$$\text{Pass } r_{ij} = \begin{pmatrix} 1 & \text{-----} & & & & & & \\ & 1 & \text{-----} & r_{ij} & & & & \\ & & 1 & & & & & \\ & & & 1 & & & & \\ & & & & \text{---} & & & \end{pmatrix}$$

Pass parameter	1	2	3	4	5	6	7	8
1	1	r ₁₂	○			○		
2		1		○	○			
3	○		1			○	○	
4		○		1				
5		○			1			
6	○		○			1	○	○
7			○			○	1	
8						○		1
Pass total N	3	3	4	2	2	5	3	2

Fail parameter	1	2	3	4	5	6	7	8
1	1					○	○	
2		1	○					
3		○	1					
4				1		○	○	○
5					1			
6	○			○		1		
7	○			○			1	
8				○				1
Fail total N	3	2	2	4	1	3	3	2

Pass + Fail total N' 6 5 6 6 3 8 6 4

FIG. 19

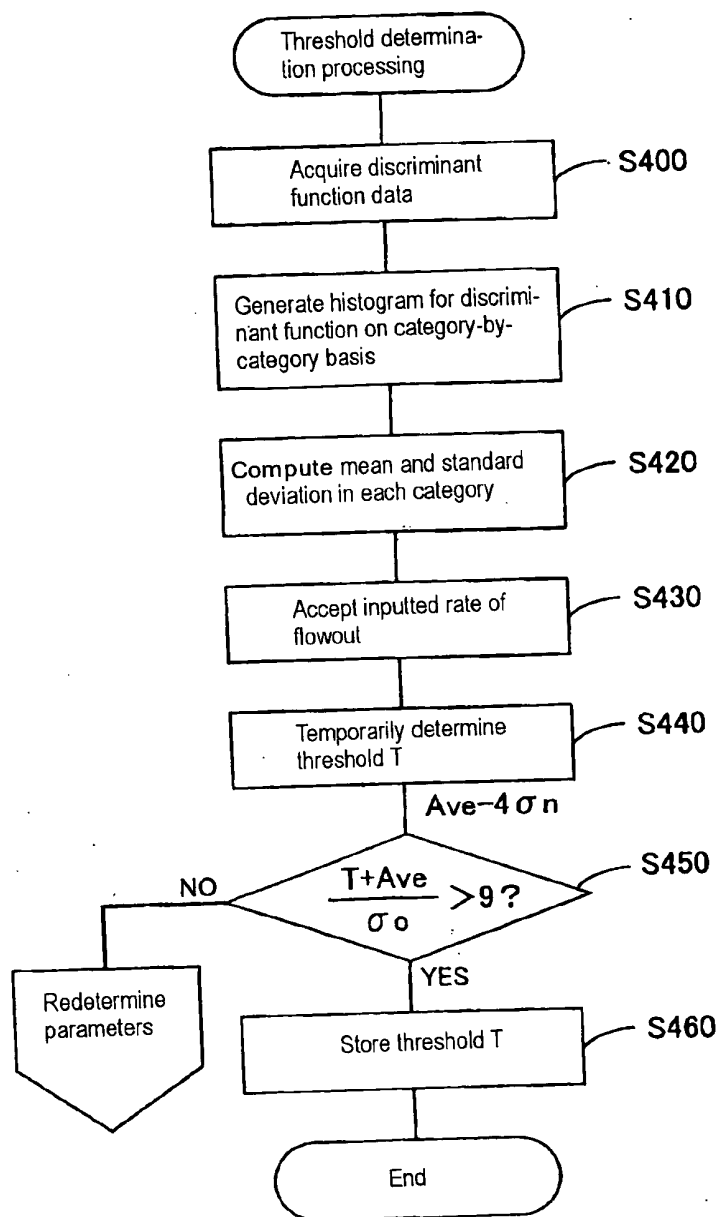


FIG. 20

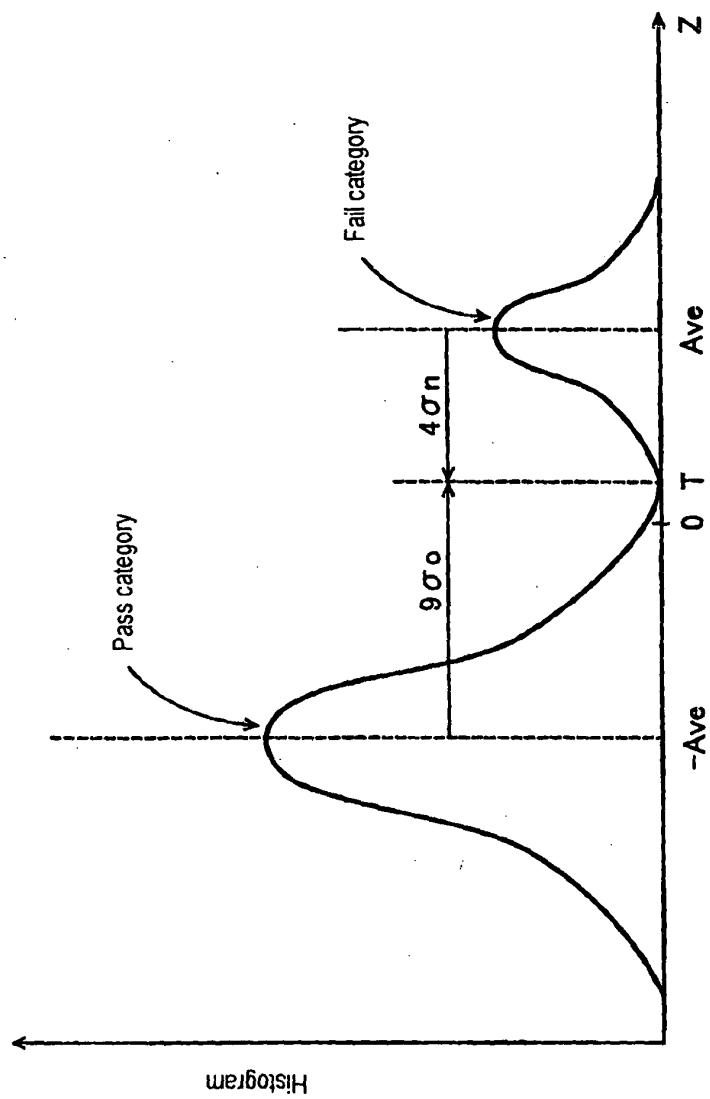


FIG. 21

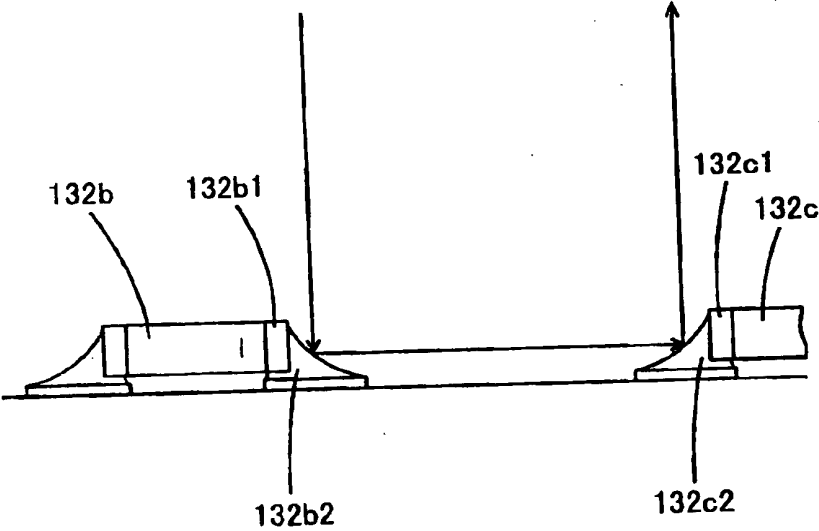


FIG. 22

